

IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE

Inventor(s): Darren John Cepulis

Confirmation No.: N/A

Application No.: N/A

Examiner: N/A

Filing Date: concurrently

Group Art Unit: N/A

Title: ROM-Embedded Debugging of Computer

Commissioner for Patents
PO Box 1450
Alexandria, VA 22313-1450

INFORMATION DISCLOSURE STATEMENT

Sir:

This Information Disclosure Statement is submitted:

- (X) under 37 CFR 1.97(b), or
(Within three months of filing national application; or date of entry of national application; or before mailing date of first office action on the merits; whichever occurs last)
- () under 37 CFR 1.97(c) together with either a:
() Statement under 37 CFR 1.97(e), or
() a \$180.00 fee under 37 CFR 1.17(p), or
(After the CFR 1.97 (b) time period, but before final action or notice of allowance, whichever occurs first)
- () under 37 CFR 1.97 (d) together with a:
() Statement under 37 CFR 1.97(e)(1) or (2), and
() a \$180.00 fee set forth in 37 CFR 1.17(p).
(Filed after final action, a notice of allowance, on or before payment of the issue fee)

Please charge to Deposit Account **08-2025** the sum of \$0.00. At any time during the pendency of this application, please charge any fees required or credit any overpayment to Deposit Account **08-2025** pursuant to 37 CFR 1.25.

(X) Applicant(s) submit herewith Form PTO 1449 - Information Disclosure Statement together with any required copies of patents, publications or other information of which applicant(s) are aware, which applicant(s) believe(s) may be material to the examination of this application and for which there may be a duty to disclose in accordance with 37 CFR 1.56.

() A concise explanation of the relevance of foreign language patents, foreign language publications and other foreign language information listed on PTO Form 1449, as presently understood by the individuals(s) designated in 37 CFR 1.56 (c) most knowledgeable about the content is given on the attached sheet, or where a foreign language patent is cited in a search report or other action by a foreign patent office in a counterpart foreign application, an English language version of the search report or action which indicates the degree of relevance found by the foreign office is listed on form PTO 1449 and is enclosed herewith.

It is requested that the information disclosed herein be made of record in this application.

"Express Mail" label no. **ER533946829US**

Date of Deposit Feb 18, 2004

I hereby certify that this is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" service under 37 CFR 1.10 on the date indicated above and is addressed to: Commissioner for Patents, Alexandria, VA 22313-1450.

By

Typed Name: L Jon Lindsay

Respectfully submitted,

Darren John Cepulis

By

L Jon Lindsay
L Jon Lindsay

Attorney/Agent for Applicant(s)

Reg. No. **36,855**

Date: Feb 18, 2004

Telephone No.: **720-344-6189**

PATENT APPLICATION

Sheet 1 of 2

FORM PTO-1449 LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)	ATTY. DOCKET NO. 200314546-1	APPLICATION NO.	CONFIRMATION NO. N/A
APPLICANT Darren John Cepulis			
FILING DATE concurrently		GROUP N/A	

REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	5,321,828	6/14/1994	Phillips , et al.	
	1B	5,555,783	9/17/1996	Pienta	
	1C	5,935,006	8/10/1999	Nakajima	
	1D	6,023,727	2/8/2000	Barrett , et al.	
	1E	6,067,614	5/23/2000	Masaru	
	1F	6,154,857	11/28/2000	Mann	
	1G	6,167,536	12/26/2000	Mann	
	1H	6,651,243	11/18/2003	Berry , et al.	
	1I	6,658,416	12/2/2003	Hussain , et al.	
	1J	6,658,557	12/2/2003	McCoy , et al.	
	1K	6,658,651	12/2/2003	O'Brien , et al.	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	PUBLICATION DATE	NAME OF PATENTEE OR APPLICANT	Pages/Columns/Lines Where Relevant Passages/Figures Appear	Check if Translation attached
	1L					
	1M					
	1N					
	1O					
	1P					

OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, etc.)

	1Q	DS-750 Development Tool for 87C750 Family of Microcontrollers, Product Brochure
	1R	"NStrace: A bus-driven instruction trace tool for PowerPC microprocessors," IBM Journal of Research and Development, Performance analysis and its impact on design; Volume 41, Number 3, 1997, http://www.research.ibm.com/journal/rd/413/sandon.html
	1S	

EXAMINER

DATE CONSIDERED

PATENT APPLICATION

Sheet **2** of **2**

FORM PTO-1449

**LIST OF PATENTS AND PUBLICATIONS FOR
APPLICANT'S INFORMATION DISCLOSURE
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(Use several sheets if necessary)

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200314546-1

APPLICATION NO.

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APPLICANT

Darren John Cepulis

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concurrently

GROUP

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REFERENCE DESIGNATION

U.S. PATENT DOCUMENTS

EXAMINER INITIAL		DOCUMENT NUMBER	PUBLICATION DATE	NAME	Pages, Columns, Lines Where Relevant Passages or Figures Appear
	1A	6,662,313	12/9/2003	Swanson, et al.	
	1B				
	1C				
	1D				
	1E				
	1F				
	1G				
	1H				
	1I				
	1J				
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	1Q	
	1R	
	1S	

EXAMINER

DATE CONSIDERED